## Amendments to the Claims:

This listing of claims will replace all prior versions, and listings of claims in the application:

## **Listing of Claims:**

- Claim 1. (original) A carrier for a semiconductor die package, the carrier comprising:
  - (a) a metal layer; and
  - (b) a plurality of bumps formed in the metal layer,

wherein the carrier is for electrically coupling a semiconductor die to a circuit substrate.

- Claim 2. (original) The carrier of claim 1 wherein the metal layer comprises copper.
- Claim 3. (original) The carrier of claim 1 wherein the plurality of bumps are disposed in an array and are stamped bumps.
  - Claim 4. (original) The carrier of claim 1 further comprising:
- a die attach region, and wherein the plurality of bumps are arranged around the die attach region.
- Claim 5. (original) The carrier of claim 1 further comprising a dielectric layer, wherein the metal layer is on a dielectric layer.
- Claim 6. (original) The carrier of claim 1 wherein the metal layer includes one or more sublayers of material on a base metal.
- Claim 7. (original) The carrier of claim 1 wherein the metal layer is discontinuous and includes a plurality of etched conductive lines that lead to the plurality of bumps.
- Claim 8. (original) The carrier of claim 1 wherein each bump has a conical angle of about 40 degrees of more.
  - Claim 9. (original) The carrier of claim 1 wherein each bump has a conical shape.

- Claim 10. (original) A semiconductor die package comprising:
- (a) a carrier comprising a metal layer, a die attach region, and a plurality of bumps formed in the metal layer; and
- (b) a semiconductor die electrically coupled to the die attach region of the carrier.
- Claim 11. (original) The die package of claim 10 wherein the plurality of bumps are stamped bumps and are arranged around the die attach region, and wherein each of the bumps has a height that is greater than or equal to a thickness of the semiconductor die.
  - Claim 12. (original) The die package of claim 10 wherein the carrier comprises copper.
  - Claim 13. (original) The die package of claim 10 wherein the carrier comprises: a base metal with one or more coatings on the base metal.
- Claim 14. (original) The die package of claim 10 wherein each bump has a conical angle greater than about 40 degrees.
- Claim 15. (original) The die package of claim 10 wherein the semiconductor die comprises a vertical metal oxide semiconductor field effect transistor (MOSFET) device.
- Claim 16. (original) The die package of claim 10 wherein the semiconductor die comprises a vertical metal oxide semiconductor field effect transistor (MOSFET) device having a source region, a gate region, and a drain region, wherein the drain region is proximate to the die attach region of the carrier, and the source region and the gate region are distal to the die attach region of the carrier.
- Claim 17. (original) The die package of claim 10 wherein each stamped bump has a conical shape.
- Claim 18. (original) The die package of claim 10 wherein the bumps and the semiconductor die are at opposite sides of the carrier.

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Claim 19. (original) The die package of claim 10 wherein the bumps and the semiconductor die are at the same side of the carrier.

Claim 20. (original) A semiconductor die package comprising:

- (a) a carrier comprising metal layer, a die attach region, and a plurality of stamped bumps formed in the metal layer around the die attach region;
- (b) a semiconductor die comprising a vertical metal oxide semiconductor field effect transistor (MOSFET) device having a source region, a gate region, and a drain region, wherein the drain region is electrically coupled to and proximate to the die attach region of the carrier, and the source region and the gate region are distal to the die attach region, and wherein the plurality of stamped bumps in the carrier are arranged around the semiconductor die; and
  - (c) a plurality of solder deposits disposed on the semiconductor die.
- Claim 21. (original) The semiconductor die package of claim 20 wherein the each of the bumps has a conical angle greater than about 40 degrees or more.
- Claim 22. (original) The semiconductor die package of claim 20 wherein the carrier comprises copper.
- Claim 23. (original) The semiconductor die package of claim 20 the plurality of bumps are formed simultaneously in the metal layer.

Claims 24-28 (canceled).